

Docket No.: MUH-12823

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By: [Signature]

Date: November 3, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/685,064  
Applicant : Peter Weitz  
Filed : October 14, 2003  
Art Unit : to be assigned  
Examiner : to be assigned

Docket No. : MUH-12823  
Customer No. : 24131

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner for Patents

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 6,211,559 B1 (Zhu et al.), dated April 3, 2003;

European Patent Application EP 1 061 592 A2 (Odagawa et al.), dated December 20, 2000;

International Publication WO 99/14760 (Ramcke et al.), dated March 25, 1999;

Roy E. Scheuerlein: "Magneto-Resistive IC Memory Limitations and Architecture Implications", *Proceedings of the 7<sup>th</sup> IEEE International Nonvolatile Memory Technology Conference*, June 22-24, 1998, pp. 47-50;

S. Tehrani et al.: "Recent Developments in Magnetic Tunnel Junction MRAM", *IEEE Transactions on Magnetics*, Vol. 36, No. 5, September 2000, pp. 2752-2757;

German Examination Report dated March 22, 2001;

International Search Report dated April 8, 2003.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant. As per the Notice in 1273 OG 55 (August 5, 2003) no copies of any above-mentioned U.S. patents and U.S. patent application publications are submitted for any application filed after June 30, 2003.

Respectfully submitted,



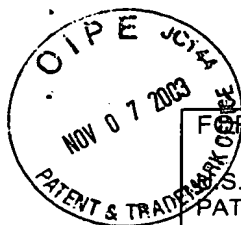
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FORM PTO-1449 (SUBSTITUTE)				Attorney Docket No.: MUH-12823				Applic. No. 10/685,064	
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE				Applicant Peter Weitz					
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Filing Date October 14, 2003				Group Art Unit	
U.S. PATENT DOCUMENTS									
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE		
	A	6,211,559 B1	04/03/03	Zhu et al.					
	B								
	C								
	D								
	E								
	F								
	G								
	H								
	I								
FOREIGN PATENT DOCUMENT									
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES   NO		
	J	1 061 592 A2	12/20/00	Europe			X		
	K	99/14760	03/25/99	WIPO				X	
	L								
	M								
	N								
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)									
	O	Roy E. Scheuerlein: "Magneto-Resistive IC Memory Limitations and Architecture Implications", <i>Proceedings of the 7<sup>th</sup> IEEE International Nonvolatile Memory Technology Conference</i> , June 22-24, 1998, pp. 47-50							
	P	S. Tehrani et al.: "Recent Developments in Magnetic Tunnel Junction MRAM", <i>IEEE Transactions on Magnetics</i> , Vol. 36, No. 5, September 2000, pp. 2752-2757							
EXAMINER				DATE CONSIDERED					
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.									